IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of OOSAWA et al.)
)
Serial Number: To be assigned)
Filed:	Concurrently herewith)
For:	SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE)
Attori	ney Docket No. HITA.0413)

Honorable Assistant Commissioner for Patents Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Prior to an examination on the merits, please amend the above-identified application as follows:

IN THE SPECIFICATION:

Please insert before the first paragraph under BACKGROUND OF THE INVENTION of page 1 of the disclosure currently on file with the following paragraph:

This application is a continuation application of U.S. application No. 10/036,460 filed on January 7, 2002.

IN THE CLAIMS

Please cancel claims 1-10.

Please add the following claims:

11. A semiconductor integrated circuit comprising:

a digital circuit formed on a semiconductor substrate; an analog circuit formed on the semiconductor substrate; an external terminal;

a selection circuit coupled to the external terminal;